Highly Connected Qubit Network Using Multimodal Circuits as Building Blocks

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Abstract. Superior inter-qubit connectivity and higher dimensional gates enable efficient implementation of quantum algorithms in quantum processors. Most superconducting quantum processors typically have nearest neighbour connectivity and use native two-gates only. Multimodal circuits like the trimon, offer an alternative implementation of multi-qubit blocks with high fidelity native multi-qubit gates. Here, we experimentally demonstrate a multi-qubit gate between a multimodal system and a conventional transmon qubit and explain how it can be scaled to make a highly connected network of multi-modal systems.

Keywords: Superconducting Qubits, Quantum Processor, Multi-qubit Gate

Superconducting circuits are one of the most promising candidates in the race for building a quantum computer with the demonstration of several intermediate scale quantum processors^[1,2] and recent achievement of quantum supremacy on a programmable 53 qubit processor^[3]. Since most superconducting processors implement only nearest neighbour coupling, one needs to apply several additional gates to perform an operation between qubits which are not directly connected. Such limited connectivity often limits the performance of most quantum algorithms and quantum simulations^[4,5].

We recently introduced multimodal circuits^[6,7] which implement all-to-all connected qubit networks with native multi-qubit gates. The Nnormal modes of such circuits give rise to Nqubits. Strong always on coupling allows one to implement fast N-qubit gates as well. We recently demonstrated a three-qubit processor^[8] using a three-mode circuit nicknamed the trimon which highlights the advantages of native three-qubit gates and higher connectivity. Based on that, we propose an alternative strategy (see Fig.1) for scaling up quantum processors, using multi-modal circuits as building blocks for superior connectivity among qubits.

In this work^[9] we demonstrate a simple multiqubit system, containing only one transmon^[10] and a two-qubit multi-modal circuit, called dimon. We couple the two with a 3D waveguide bus cavity, mediating an exchange interaction between one of the modes of the dimon and the transmon. We use this interaction and implement the multi-qubit operation by driving the transmon at the transition frequency of the coupled dimon mode. This turns on a cross resonance interaction^[11,] that approximately implements a conditional ZX operation in the circuit.

We first experimentally study the effect of cross resonance drive on the system as a function of the detuning between the two qubits and identify the optimum range of detuning for the system. In the next experiment, we use the optimum detuning and carefully calibrate the cross-resonance interaction to implement a ZX gate with a fidelity exceeding 97%, measured by randomized benchmarking^[12]. The fidelity is limited due qubit decoherence (limiting the fidelity to 97.9%) and a small cross-kerr (ZZ) interaction^[13] between the dimon and the transmon. This result paves the way towards building quantum processors with multi-modal circuit as building blocks.



Fig.1. Multimodal circuits as building blocks for quntum processors. One of the dipolar modes is coupled with the transmon. An extension of the design for a highly connected intermediate scale quantum processor.

References

[1] IBM Quantum Experience,

[2] C. Neill, et al., <u>Science360, 195 (2018)</u>

[1] F. Arute, et al., <u>Nature 574, 505 (2019)</u>

[4] A. W. Cross, L. S. Bishop, S. Sheldon, P. D. Nation, J. M. Gambetta, <u>Physical Review A100, 032328</u> (2019)

[5] Adam Holmes, Sonika Johri, Gian Giacomo Guerreschi, James S Clarke and A Y Matsuura, <u>Quantum Science andTechnology5</u>, 025009 (2020)

[6] T. Roy, S. Kundu, M. Chand, S. Hazra, N. Nehra, R. Cosmic, A. Ranadive, M. P.Patankar, K. Damle, and R. Vijay, *Phys. Rev. Appl.*7, 054025 (2017)

[7] T. Roy, M. Chand, A. Bhattacharjee, S. Hazra, S. Kundu, K. Damle, and R.Vijay, <u>Phys. Rev. A98</u>, 052318 (2018)

[8] T. Roy, S. Hazra, S. Kundu, M. Chand, M. P. Patankar, and R. Vijay, <u>Phys. Rev. Appl. 14, 014072</u> (2020)

[9] S Hazra, K V. Salunkhe, A Bhattacharjee, G Bothara, S Kundu, T Roy, M P. Patankar, and R. Vijay, <u>Appl. Phys. Lett. 116, 152601 (2020)</u> (Abstract based on this work)

[10] J. Koch, et al., Phys. Rev. A76, 042319 (2007)

[11] J. M. Chow, et al., Phys. Rev. Lett. 107, 080502 (2011)

[12] . Magesan, J. M. Gambetta, B. R. Johnson, C. A. Ryan, J. M. Chow, S. T.Merkel, M. P. da Silva, G.

A. Keefe, M. B. Rothwell, T. A. Ohki, M. B. Ketchen, and M. Steffen, <u>Phys. Rev. Lett109</u>, 080505 (2012) [13] Easwar Magesan and Jay M. Gambetta, <u>Phys. Rev. A</u>, 101:052308,(2020)